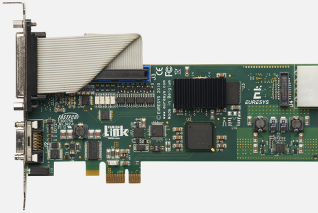


Grablink Base

用于一个基本配置 Camera Link 摄像机的图像采集卡



一览

- 对于 Camera Link Base 或 Lite 配置摄像机
- 直接兼容市场上数以百计的 Camera Link 摄像机
- 支持 PoCL, 通过 Camera Link 供电
- ECC0: 延长的 Camera Link 线缆长度
- PCIe x1 总线: 200 MB/s 持续传输带宽
- 特征丰富的 10 条数字 I/O 线

优势

ECC0: Camera Link 延长线操作

- 使用更长的 Camera Link 线缆, 可达 15 米长!
- 下载该文档了解更多信息

直接兼容市场上数以百计的 Camera Link 摄像机

检查我们的摄像机兼容页面 (在支持菜单中) 以下载相关 Cam 文件。

通用 I/O 线

- 兼容多种传感器和运动编码器。
- 高速差分输入: 正交运动编码器, 支持高达 5 MHz。
- 隔离电流检测输入: 接受 5V、12V、24V 信号电压, 最高 50 kHz, 各个电隔离高达 500 VAC RMS。
- 隔离触式输出。

高性能 DMA (直接存储器存取)

- 直接传送到用户分配的内存
- 硬件分散 — 聚集支持
- 64 位寻址能力

区域扫描触发功能

- 触发器用于在零件就位时启动采集。硬件触发器来自 Grablink 的 I/O 线。软件触发器来自于应用程序。
- 可选的触发器延时, 用于按可编程的时间来推迟采集。
- 触发抽取功能允许跳过某些触发器。
- 摄像机曝光控制允许应用来控制摄像机的曝光时间。
- 在适当的时间启动采集时, Grablink 卡生成一个信号来控制连接到一条输出线的照明设备。

线扫描触发功能

Grablink 支持连续滚网扫描（以检查无限、连续移动的表面而不丢失行）和离散的目标扫描（以采集在摄像机前方移动的目标图像）。

- 触发器用于在零件就位时启动采集。硬件触发器来自主板 I/O 线。软件触发器来自于应用程序。
- 启动以后，采集将：
 - 无穷继续下去（对于大幅物体检查应用程序）
 - 继续进行可编程的行数（以采集已知长度的目标图像）
 - 继续进行直至收到结束触发信号（以采集可变长度的目标图像）
- 可选的触发器延时，用于按可编程的行数来推迟开始采集。

线扫描触发功能

- Grablink 图像采集卡根据从运动编码器接收到的信号来控制摄像机扫描率。如果零件移动速度变快，摄像机的采集线率将增大。如果零件移动速度变慢，摄像机的采集线率将减小。
- Grablink 板解读来自正交运动编码器的 A/B 信号，了解零件向哪个方向（向前或向后）移动。
- 可以选择是否指示 Grablink 仅当对象向前移动或向后移动时获取路径。
- 监测到向后运动时，名为“向后动作取消”的功能即停止采集。当在采集中断位置再次向前运动时，自动恢复行采集。
- 速率转换器能够让摄像机以任何低于或高于运动编码器分辨率的可编程分辨率来采集行。这就为设计师在应用程序开发过程中提供了惊人的自由度和灵活性。
- 速率除法器能够让摄像机以任何低于或高于运动编码器分辨率的分辨率来采集行。它将进入的编码器信号的频率除以一个可编程的整数。

带有速率转换器的柔性线扫描摄像机操作。

- 速率转换器是一个智能的、可编程的倍频器/分频器。
- 用于运动编码器和线扫描摄像机，允许用户选择该图像中的像素纵横比。
- 它提供了一种方法来校准采集链以轻松达到正方形（1:1 纵横比）像素。

提供 Windows 和 Linux 驱动程序

应用

电子制造业的机器视觉

用于检查机器的高速图像采集。

Coaxlink 和 Grablink 卡是可靠的工业图像采集卡，从最快的数码摄像机提供强大和稳定的图像采集。它们的特点是精确的摄像机控制和同步功能。

- AOI（自动光学检测）机
- 3D SPI（锡膏检测）机
- 3D 引线/球型检查机

用于检查机的极高分辨率的线扫描图像采集

Coaxlink 和 Grablink 卡是可靠的工业图像采集卡，从最快的数码摄像机提供强大和稳定的图像采集。它们的特点是精确的线扫描摄像机控制和同步功能。

- 平板显示检测
- 太阳能电池检查

一般制造业的机器视觉

用于检查机的高帧率图像采集

玻璃检测：瓶、小瓶

表面检测机的线扫描图像采集

Coaxlink 和 Grablink 卡是可靠的工业图像采集卡，从最快的数码摄像机提供强大和稳定的图像采集。它们的特点是精确的线扫描摄像机控制和同步功能。

用于纺织品检测的线扫描图像采集

印刷业的机器视觉

用于印刷检查机的高速线扫描图像采集

- 包装打印检查
- 标签打印检查

规范

Mechanical

Form Factor	PCI Express card
Format	Low profile, half length, 1-lane PCI Express card
Cooling method	Air-cooling, passive heatsink
Mounting	For insertion in a low-profile or standard height, 1-lane or higher, PCI Express card slot
Connectors	<ul style="list-style-type: none"> • 'A' on bracket: <ul style="list-style-type: none"> – 26-position Shrunk Delta Ribbon (SDR) socket – Camera Link Base connector • 'EXTERNAL I/O' on standard bracket: <ul style="list-style-type: none"> – 25-pin 2-row female sub-D connector – I/O lines and power output • 'INTERNAL I/O' on PCB: <ul style="list-style-type: none"> – 26-pin 2-row 0.1" pitch pin header with shrouding – I/O lines and power output • 'POWER INPUT' on module: <ul style="list-style-type: none"> – 4-pin MOLEX power socket – 12 VDC power input for PoCL camera and I/O power
Dimensions	L 167.65 mm x H 68.90 mm L 6.6 in x H 2.71 in
Weight	98 g, 3.46 oz

Host bus

Standard	PCI Express 1.0
Link width	1 lane
Link speed	2.5 GT/s (PCIe 1.0)
Maximum payload size	1024 bytes
DMA	32- and 64-bit
Peak delivery bandwidth	256 MB/s
Effective (sustained) delivery bandwidth	<ul style="list-style-type: none"> • Up to 200 MB/s for a PCI Express payload size of 256 bytes • Up to 180 MB/s for a PCI Express payload size of 128 bytes
Power consumption	Max. 4.5 W; Typ. 3.8 W (0.34 A @ 3.3V; 0.22 A @ +12V)

Camera / video inputs

Interface standard(s)	Camera Link 2.0
Connectors	1 Shrunk Delta Ribbon (SDR) Miniature Camera Link (MiniCL)
ECCO - Extended Camera Link Cable Operation	ECCO
Number of cameras	One Base or Lite camera
Line-scan cameras supported	Yes
Maximum aggregated camera data transfer rate	2.04 Gbit/s (255 MB/s)
Camera Link configuration	Base or Lite
Camera Link clock frequency range	From 20 MHz up to 85 MHz
PoCL (Power over Camera Link)	One PoCL SafePower compliant controller with overload, over-voltage and short-circuit protections
Camera types	<ul style="list-style-type: none">• Gray-scale and color (RGB and Bayer) cameras• Area-scan and line-scan cameras
Camera pixel formats supported	Monochrome, Bayer, and RGB (PFNC names): <ul style="list-style-type: none">• Mono8, Mono10, Mono12, Mono14, Mono16• BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG• RGB8

Area-scan camera control

Trigger	<ul style="list-style-type: none">• Precise control of asynchronous reset cameras, with exposure control.• Support of camera exposure/readout overlap.• Support of external hardware trigger, with optional delay and trigger decimation.
Strobe	<ul style="list-style-type: none">• Accurate control of the strobe position for strobed light sources.• Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger	<ul style="list-style-type: none">• Precise control of start-of-scan and end-of-scan triggers.• Support of external hardware trigger, with optional delay.• Support of infinite acquisition, without missing line, for web inspection applications.
Line trigger	<ul style="list-style-type: none">• Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.• Rate Converter tool for fine control of the pixel aspect ratio.• Rate Divider tool
Line strobe	<ul style="list-style-type: none">• Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory	64 MB (32 MB for image data)
Image data stream processing	<ul style="list-style-type: none">• Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSb or MSb
Input LUT (Look-Up-Table)	<ul style="list-style-type: none">• Monochrome: 8-bit, 10-bit or 12-bit per pixel, up to 500 MPixel/s• RGB: 3x8-bit per pixel, up to 125 MPixel/s
Geometrical operators	<ul style="list-style-type: none">• Cropping (not available for 2YE cameras)• Left/right and up/down mirroring
Bayer CFA to RGB decoder	<ul style="list-style-type: none">• Advanced interpolation method using average and median functions on a 3x3 kernel• Up to 125 MPixel/s

General Purpose Inputs and Outputs

Number of lines	10 I/O lines: <ul style="list-style-type: none">• 2 differential inputs (DIN)• 4 isolated inputs (IIN)• 4 isolated outputs (IOUT)
Usage	<ul style="list-style-type: none">• The input lines can be used by the acquisition channel as:<ul style="list-style-type: none">– Camera frame trigger source (area-scan only)– Acquisition sequence trigger source (area-scan only)– Camera line trigger source (line-scan only)– Page acquisition trigger source (line-scan only)– Page acquisition end trigger source (line-scan only)– (Quadrature) motion encoder input (line-scan only)• The IOUT 1 output line can be used by the acquisition channel as:<ul style="list-style-type: none">– Illumination strobe output• All the input lines can be used as general purpose inputs• All the output lines can be used as general purpose outputs
Electrical specifications	<ul style="list-style-type: none">• DIN: High-speed differential inputs compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers• IIN: Isolated current-sense inputs with wide voltage input range up to 30V, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers• IOUT: Isolated contact outputs compatible with 30V / 100mA loads
Filter control	<ul style="list-style-type: none">• Glitch removal filter available only on input lines used as trigger sources• Configurable with five time constants:<ul style="list-style-type: none">– 100 ns, 500 ns, and 2.5 μs for trigger / page trigger / page end trigger sources– 40 ns, 100 ns, 200 ns, 500 ns, 1 μs, 5 μs, 10 μs for line trigger sources
Power output	Non-isolated, +5V, 1A and +12V, 1A, with electronic fuse protection

Software

Host PC Operating System	<ul style="list-style-type: none">• Microsoft Windows 10, 8.1, 8, 7, XP• Microsoft Windows Server 2012 R2, 2012, 2008 R2• Linux Kernel versions from 2.6 up to 3.13, compatible with a wide range of distributions• 32- and 64-bit versions
APIs	<ul style="list-style-type: none">• MultiCam 32-bit and 64-bit binary libraries (Windows and Linux), for ISO-compliant C/C++ compilers• DirectShow filters (Windows only) for Microsoft Visual C++ compilers (32-bit only)

Environmental conditions

Operating ambient air temperature	0 to +50 °C / +32 to +122 °F
Operating ambient air humidity	10 to 90% RH non-condensing
Storage ambient air temperature	-20 to +70 °C / -4 to +158 °F
Storage ambient air humidity	10 to 90% RH non-condensing

Certifications

Electromagnetic - EMC standards	<ul style="list-style-type: none">• The European Council EMC Directive 2004/108/EC• The Unites States FCC rule 47 CFR 15
EMC - Emission	<ul style="list-style-type: none">• EN 55022:2010 Class B• FCC 47 Part 15 Class B
EMC - Immunity	<ul style="list-style-type: none">• EN 55024:2010 Class B• EN 61000-4-2• EN 61000-4-3• EN 61000-4-4• EN 61000-4-5• EN 61000-4-6
Flammability	PCB compliant with UL 94 V-0
RoHS	Compliant with the European Union Directive 2011/65/EU (ROHS2)
REACH	Compliant with the European Union Regulation No 1907/2006
WEEE	Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description	<ul style="list-style-type: none">• 1624 - Grablink Base
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